

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicant(s): W. Rhee et al.  
Docket No.: YOR920030258US1  
Serial No.: 10/697,751  
Filing Date: October 30, 2003  
Group: 2816  
Examiner: Ryan C. Jager

Title: Voltage-Controlled Delay Circuit Using  
Second-Order Phase Interpolation

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**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

Sir:

In response to the present Office Action dated August 8, 2008, Applicants present the following amendments and remarks for consideration: